Finite Models for Safety Verification

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Outline

Preamble

- Reachability as deducibility
- Verification via countermodel finding
 - Lossy Channel Systems
 - Cache Coherence Protocols
 - Linear Systems of Automata and Monotonic Abstraction
 - Regular Model Checking
 - Regular Tree Model Checking
 - Safety for general TRS and Tree Automata Completion

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- *w*_o = *b*
- *w*₁ = *a*
- $w_{i+2} = w_{i+1}w_i$
- b, a, ab, aab, abaab, aababaab, abaabaabaabaab . . .

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- Answer: Let's apply FO logic ...

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FO theory FIB:

•
$$(x * y) * z = x * (y * z)$$

• $R(x,y) \rightarrow R(y,x*y)$

Proposition

If w is a Fibonacci word then $FIB \vdash \exists xR(t_w, x)$

Here t_w denote a term encoding of w, i.e. $t_{aba} = (a * b) * a$

Corollary

If FIB $\not\vdash \exists x \exists z \exists y R(z * b * b * y, x)$ then there is no Fibonacci word with bb as a subword

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Now to show $FIB \not\vdash \exists x \exists z \exists y R(z * b * b * y, x)$ we are looking for

- Finite countermodels for $FIB \rightarrow \exists x \exists z \exists y R(z * b * b * y, x)$, or equivalently, for
- Finite models for $FIB \land \neg \exists x \exists z \exists y R(z * b * b * y, x)$

To find a model we apply generic finite model finding procedure, e.g. impemented in Mace4 finite model finder by W.McCune (see demonstration)

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- A model of size 5 is found in 0.05s. The property is proved!
- To show that 'aaa' is not a subword of any Fib. word a model of size 11 can be found in \approx 45s

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In this talk:

the same idea/approach can be applied to (surprisingly) large classes of infinite state and parameterized safety verification problems.

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- Many problems in verification can be naturally formulated in terms of *reachability* within transition systems;
- We propose to use deducibility (or derivability) in first-order predicate logic to model reachability in transition systems of interest;
- Then verification can be treated as theorem (dis)proving in classical predicate logic;
- Many automated tools (provers and model finders) are readily available.

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- Let $S = \langle S, \rightarrow \rangle$ be a transition system with the set of states S and transition relation \rightarrow
- Let e : s → φ_s be encoding of states of S by formulae of first-order predicate logic, such that
 - the state s' is reachable from s, i.e. s →* s' if and only if φ_{s'} is the logical consequence of φ_s, that is φ_s ⊨ φ_{s'} and φ_s ⊢ φ_{s'}.
- Under such assumptions:
 - Establishing reachability \equiv theorem proving
 - Establishing non-reachability \equiv theorem disproving

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- Safety \equiv non-reachability of "bad" states
- \bullet Verification of safety properties \equiv theorem disproving
- To disprove $\varphi \models \psi$ it is sufficient to a find a countermodel for $\varphi \rightarrow \psi$, or which is the same a model for $\varphi \land \neg \psi$
- In general, such a model can be inevitably infinite and the set of satisfiable first-order formulae is not r.e.
- One can not hope for full automation here
- Our proposal: use automated finite model finders/builders

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• For the verification of safety the weaker assumption on the encoding is sufficient:

• $s \rightarrow^* s' \Rightarrow \varphi_s \vdash \varphi_{s'}$

- For the verification of parameterized systems general idea of reachability as deducibility should be suitably adjusted
 - depends on particular classes of systems
 - unary or binary predicates modeling reachability can be used

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Origins

- The idea of using finite model finders for verification is not new (thanks to anonymous referees of FMCAD 2010 conference!)
- It was proposed and developed in the area of verification of security protocols in the following papers (at least):
 - C. Weidenbach Towards an Automatic Analysis of Security Protocols in First-Order Logic, in H. Ganzinger (Ed.): CADE-16, LNAI 1632, pp. 314–328, 1999.
 - Selinger, P.: Models for an adversary-centric protocol logic. Electr. Notes Theor. Comput. Sci. 55(1) (2001);
 - Goubault-Larrecq, J.: Towards producing formally checkable security proofs, automatically. In: Computer Security Foundations (CSF), pp. 224238 (2008)
 - Jan Jurjens and Tjark Weber, Finite Models in FOL-Based Crypto-Protocol Verification. Foundations and Applications of Security Analysis, LNCS 5511, 2009.

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What is new then?

- Countermodel finding based verification methods are practically efficient for the verification of various classes of infinite state and parameterized systems:
 - lossy channel systems
 - cache coherence protocols
 - parameterized linear arrays of finite state automata
 - etc.
- Completeness (for lossy channel systems verification)
- Relative completeness wrt to regular model checking (RMC); regular tree model checking (RTMC); tree automata completion techniques
- In many cases no specialized model finding procedures is needed. Generic MACE4 finite model finder by W.McCune has been successfully used to verify above systems

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Parameterized linear arrays of automata

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- Taken from the paper Parosh Aziz Abdulla, Giorgio Delzanno, Noomene Ben Henda, Ahmed Rezine. Monotonic Abstraction: on Efficient Verification of Parameterized Systems. *Int. J. Found. Comput. Sci.* 20(5): 779-801 (2009)
- Operates on the parameterized linear array of finite state automata

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The protocol is specified as a parameterized system $\mathcal{ME} = (Q, T)$, where $Q = \{green, black, blue, red\}$ is the set of local states of finite automata, and T consists of the following transitions:

- $\forall_{LR} \{ green, black \} : green \rightarrow black$
- $black \rightarrow blue$
- $\exists_L \{ black, blue, red \} : blue \rightarrow blue$
- $\forall_L \{green\} : blue \rightarrow red$
- red \rightarrow black
- $black \rightarrow green$

The correctness condition: if the protocol starts with all states being *green* it will never get to a state where there are two or more automata in the *red* state

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Translation to the first-order logic,I

•
$$(x * y) * z = x * (y * z)$$

•
$$e * x = x * e = x$$

(* is a monoid operation and e is a unit of a monoid)

- G(e)
- $G(x) \rightarrow G(x * green)$

(specification of configurations with all green states)

- *GB*(*e*)
- $GB(x) \rightarrow GB(x * green)$
- $GB(x) \rightarrow GB(x * black)$

(specification of configurations with all states being green or black)

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•
$$G(x) \rightarrow R(x)$$

(initial states assumption: "allgreen" configurations are reachable)

•
$$(R((x * green) * y) \& GB(x) \& GB(y)) \rightarrow R((x * black) * y)$$

• $R((x * black) * y) \rightarrow R((x * blue) * y)$
• $R((x * blue) * y) \& (x = (z * black) * w) \rightarrow R((x * blue) * y)$
• $R((x * blue) * y) \& (x = (z * blue) * w) \rightarrow R((x * blue) * y)$
• $R((x * blue) * y) \& (x = (z * red) * w) \rightarrow R((x * blue) * y)$
• $R((x * blue) * y) \& G(x) \rightarrow R((x * red) * y)$
• $R((x * red) * y) \rightarrow R((x * black) * y)$
• $R((x * black) * y) \rightarrow R((x * green) * y)$

(specification of reachability by one step transitions from T; one formula per transition, except the case with existential condition, where three formulae are used)

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- If a configuration \bar{c} is reachable in \mathcal{ME} then $\Phi_{\mathcal{P}} \vdash R(t_{\bar{c}})$
- To establish safety property of the protocol (mutual exclusion) it does suffice to show that
 Φ_P ∀ ∃x∃y∃zR((((x * red) * y) * red) * z).
- Delegate the latter task to the finite model finder MACE4 (see demonstration)

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- Delegate the latter task to the finite model finder MACE4 (see demonstration)
- It takes approx. 0.01s to find a countermodel and verify the safety property!

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- Take a configuration \bar{c} of the protocol, consider its term representation $t_{\bar{c}}$
- The following property is an invariant of the system:

$$[t_{\overline{c}}] \in [R]$$

Here [...] denote the interpretation in the (counter)model.

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Model and Invariant

The domain *D* of the model is a four element set $\{0, 1, 2, 3\}$. Interpretations of constants: [black] = [blue] = 0, [e] = [green] = 1, [red] = 2. Interpretations of unary predicates: $[G] = \{1\}$; $[GB] = \{0, 1\}$; $[R] = \{0, 1, 2\}$.

The interpretation of the binary function * is given by the following table

| | 0 | 1 | 2 | 3 |
|---|---|---|---|---|
| 0 | 0 | 0 | 2 | 3 |
| 1 | 0 | 1 | 2 | 3 |
| 2 | 2 | 2 | 3 | 3 |
| 3 | 3 | 3 | 3 | 3 |

Invariant property which holds for any reachable configuration \bar{c} :

$$[t_{\bar{c}}] \in [R] = \{0, 1, 2\}$$

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Theorem (2010)

If the safety of parameterized linear system of automata can be demonstrated by monotonic abstraction method then it can be demonstrated by FCM too.

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| Problem | |
|-----------|---|
| Given: | A parameterized system $\mathcal{P} = (Q, T)$, a set $In \subseteq C$ of initial configurations, a set $B \subseteq C$ of bad configurations. |
| Question: | Are there any configurations $c \in In$ and $c' \in B$ such that c' is reachable from c in \mathcal{P} , i.e. for which $c \rightarrow_{\mathcal{P}}^{*} c'$ holds? |

A negative answer for the above question means the safety property ("not B") holds for the parameterized system. Further assumptions:

- $I = q_o^*$ for $q_o \in Q$
- The set B of bad configurations is defined by a finite set of words F ⊆ Q*: B = {īc | ∃w̄ ∈ F ∧ w̄ ≤ c̄}, where w̄ ≤ w̄' denotes that w̄ is a (not necessarily contiguous) subword of w̄'

Abdulla et al 2009

Given a parameterized system $\mathcal{P} = (Q, T)$ and the corresponding transition relation $\rightarrow_{\mathcal{P}}$ on the configurations within \mathcal{P} . The monotonic abstraction $\rightarrow_{\mathcal{P}}^{\mathcal{A}}$ of $\rightarrow_{\mathcal{P}}$ is as follows. For two configurations \bar{c} and $\bar{c'}$ $\bar{c} \rightarrow_{\mathcal{P}}^{\mathcal{A}} \bar{c'}$ holds iff either

•
$$\bar{c}
ightarrow_{\mathcal{P}} \bar{c'}$$
 holds, or

- there is a transition $t = \forall_L Jq \rightarrow q'$ in T, $\bar{c} = \bar{c}_l \ q \ \bar{c}_r$ and $\bar{c'} = reduct_J(\bar{c}_l) \ q' \ \bar{c}_r$
- there is a transition $t = \forall_R Jq \rightarrow q'$ in T, $\bar{c} = \bar{c}_l \ q \ \bar{c}_r$ and $\bar{c'} = \bar{c}_l \ q'$ reduct_J (\bar{c}_r)
- there is a transition $t = \forall_{LR} Jq \rightarrow q'$ in T, $\bar{c} = \bar{c}_l \ q \ \bar{c}_r$ and $\bar{c'} = reduct_J(\bar{c}_l) \ q' \ reduct_J(\bar{c}_r)$

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Symbolic backward reachability algorithm Abdulla et al 2009

- $U_0 = B$
- $U_{i+1} = U_i \cup Pre(U_i)$

where $Pre(X) = \{ \overline{c} \mid \exists \overline{c}' \in X \land \overline{c} \rightarrow_{\mathcal{P}}^{\mathcal{A}} \overline{c}' \}.$

- This iterative process is guaranteed to stabilize, i.e $U_{n+1} = U_n$ for some finite n.
- Once the process stabilized the resulting U consists of all configurations from which some bad configuration can be reached via →^A_P.
- Then the check is performed on whether *Init* ∩ U = Ø. If this condition is satisfied then the safety is established, for no bad configuration can be reached from initial configurations via →^A_P and, a fortiori, via →_P.

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- If the safety holds then \overline{U} (complement of U) is an *invariant* of the system sufficient to prove the safety. Indeed, it subsumes *lnit* and is closed under reachability.
- U has a finite set of generators and therefore is a *regular* set. It follows that \overline{U} is a *regular* set too.

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Theorem (on regular invariants)

Given a parameterized system $\mathcal{P} = (Q, T)$ and the set of bad configurations $B = \{ \overline{c} \mid \exists \overline{w} \in F \land \overline{w} \leq \overline{c} \}$. Then the following two conditions are equivalent:

(1) There exists a regular set of configurations Inv such that

- Reach \subseteq Inv where Reach = { $y \mid \exists x (x \in Init \land x \rightarrow_{\mathcal{P}}^* y)$ }
- Inv is closed under reachability, that is

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x \in Inv \land x \to_{\mathcal{P}} y \Rightarrow y \in Inv
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• Inv \cap B = \emptyset
```

and

(2) There exists a finite model for $\Phi_{\mathcal{P}} \land \neg \Psi_{F}$

Proof.

Uses an algebraic characterization of regular languages in terms of inverse homomorphic images of subsets of finite monoids

 $\bullet\,$ If the safety for ${\cal P}$ holds and can be shown by the monotonic abstraction method, then

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- An existence of a finite countermodel for FO encoding of the problem, which means

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- $\bullet\,$ If the safety for ${\cal P}$ holds and can be shown by the monotonic abstraction method, then
- There exists a regular invariant, which implies
- An existence of a finite countermodel for FO encoding of the problem, which means
- Safety can be established by FCM (finite countermodel method) if a complete finite model building procedure is used

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Subsets of configurations



Figure: Subsets of configurations in general position

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FCM is stronger than monotonic abstraction

The parameterized system (Q, T) where $Q = \{q_0, q_1, q_2, q_3, q_4\}$ and where T includes the following transition rules

- $\bigcirc q_1 \rightarrow q_2$

$$\exists_{LR} \{q_2\} : q_3 \to q_4$$

$$\bigcirc q_4 \rightarrow q_0$$

satisfies mutual exclusion for state q_4 , but this fact *can not* be established by the monotonic abstraction method.

Using FCM we have verified mutual exclusion for this system, demonstrating that FCM method is stronger than monotone abstraction. Mace4 has found a finite countermodel of the size 6 in 341s.

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Theorem (2011)

If the safety of a tree-shape parameterized system can be demonstrated by regular tree model checking method then it can be demonstrated by FCM too.

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Theorem (2011)

If the safety of a tree-shape parameterized system can be demonstrated by regular tree model checking method then it can be demonstrated by FCM too.

Theorem (2011, RTA 2012)

If the safety of a term rewriting system can be demonstrated by tree automata completion technique then it can be demonstrated by FCM too.

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In all cases the proofs of relative completeness results rely upon existence of regular invariants, that is regular sets (of words or trees) subsuming all reachable states and disjoint with all unsafe states.

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Parameterized tree-like systems

• Verifcation of safety (a system never goes into a failure state)

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- Verifcation of safety (a system never goes into a failure state)
- Tree-like systems: protocols and algorithms operating in the networks with tree topology

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- Systems are specified by tree transducers, initial and unsafe configurations are specified by tree automata

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- Parameterized verification: safety holds for all sizes of networks

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- Regular Tree Model Checking (RTMC) is a traditional approach

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- Tree-like systems: protocols and algorithms operating in the networks with tree topology
- Systems are specified by tree transducers, initial and unsafe configurations are specified by tree automata
- Parameterized verification: safety holds for all sizes of networks
- Regular Tree Model Checking (RTMC) is a traditional approach
- We show that FCM method using disproving in first-order logic can be used instead

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Tree Automata and Transducers

Definition

A tree automaton over a ranked alphabet Σ is a triple $A = (Q, F, \delta)$, where Q is a finite set of states, $F \subseteq Q$ is a set of final states, and δ is a transition relation, represented by a finite set of rules of the form $(q_1, \ldots, q_p) \rightarrow^f q$, where $f \in \Sigma_p$ and $q_1, \ldots, q_p, q \in Q$. A tree automaton over an alphabet Σ^2 is called *tree transducer*.

- The language *L*(*A*) of trees accepted by an automaton *A* is defined as usual.
- For a tree transducer D over an alphabet Σ^2 one-step transition relation $R_D \subseteq T(\Sigma) \times T(\Sigma)$ is defined as $R^D = \{(T, T') \mid T \times T' \text{ is accepted by } D\}.$
- R^i denotes the *i*th power of R i.e. *i* compositions of R. $R^* = \bigcup_{i \ge 0} R^i$.
- For any $L \subseteq T(\Sigma)$ and $R \subseteq T(\Sigma) \times T(\Sigma)$ we denote by $L \star R$ the set $\{y \mid \exists x(x, y) \in L \times T(\Sigma) \cap R\}$.

Regular Tree Model Checking

- Regular Tree Model Checking (RTMC) is a general method for the verification of parameterized systems that have tree topology [Abdulla et al, 2002]
- Regular Tree Model Checking deals with the following basic verification task.

Problem

Given two tree automata A_I and A_U over an alphabet Σ and a tree transducer D over Σ^2 . Does $(L(A_I) \star R_D^*) \cap L(A_U) = \emptyset$ hold?

The verification in RTMC proceeds by producing a tree transducer *TR* approximating *R*^{*}_D from above, that is *R*^{*}_D ⊆ *L*(*TR*), and showing the emptiness of the set (*L*(*A_I*) ★ *L*(*TR*)) ∩ *L*(*A_U*)

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From RTMC to First-Order Logic

- We show that basic RTMC verification problem can be reduced to a purely logical problem of disproving a first-order formula
- Assume we are given an instance of the basic verification problem (over ranking alphabet Σ), that is
 - a tree automaton A_I = (Q_I, F_I, δ_I) accepting a regular set of initial states;
 - a tree automaton A_U = (Q_U, F_U, δ_U) accepting a regular set of unsafe states;
 - a tree transducer $D = (Q_D, F_D, \delta_D)$ representation one-step transition relation R_D .
- Now define a set formulae of first-order predicate logic as follows.

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- constants for all elements of $Q_I \sqcup Q_U \sqcup Q_D \sqcup \Sigma_0$;
- unary predicate symbols *lnit*⁽¹⁾, *Unsafe*⁽¹⁾;
- binary predicate symbols Init², Unsafe², R;
- a ternary predicate symbol T;
- a *p*-ary functional symbol f_{θ} for every $\theta \in \Sigma_p$

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Given any tree τ from $T(\Sigma)$ define its term translation t_{τ} by induction:

- $t_{\tau} = c$ for a tree τ with one node labeled by $c \in \Sigma_0$;
- $t_{\tau} = f_{\theta}(t_{\tau_1}, \dots, t_{\tau_p})$ for a tree τ with the root labeled by $\theta \in \Sigma_p$ and children τ_1, \dots, τ_p .

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Frist-Order translation of RTMC problem

- Now we are ready to define a translation of RTMC problem, given by tree automata A_I, A_U and a tree transducer D to a set Φ of first-order formulae.
- Φ consists of the following formulae...

- $Init^{(2)}(a,q)$ for every $a \in \Sigma_0$, $q \in Q_I$ and $\rightarrow^a q$ in δ_I ;
- $Init^{(2)}(x_1, q_1) \land \ldots \land Init^{(2)}(x_p, q_p) \rightarrow Init^{(2)}(f_{\theta}(x_1, \ldots, x_p), q)$ for every $(q_1, \ldots, q_p) \rightarrow^{\theta} q$ in δ_I ;

•
$$\vee_{q\in F_l}$$
 Init⁽²⁾ $(x,q) \rightarrow$ Init⁽¹⁾ $(x);$

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- Unsafe⁽²⁾(a,q) for every $a \in \Sigma_0$, $q \in Q_I$ and $\rightarrow^a q$ in δ_I ;
- $Unsafe^{(2)}(x_1, q_1) \land \ldots \land Unsafe^{(2)}(x_p, q_p) \rightarrow Unsafe^{(2)}(f_{\theta}(x_1, \ldots, x_p), q) \text{ for every } (q_1, \ldots, q_p) \rightarrow^{\theta} q \text{ in } \delta_I;$
- $\vee_{q\in F_l}$ Unsafe⁽²⁾ $(x,q) \rightarrow Unsafe^{(1)}(x);$

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Translation of the transducer D

• T(a, b, q) for every $\rightarrow^{(a,b)} q$ in δ_D ;

•
$$T(x_1, y_1, q_1) \land \ldots \land T(x_p, y_p, q_p) \rightarrow$$

 $T(f_{\theta_1}(x_1, \ldots, x_p), f_{\theta_2}(y_1, \ldots, y_p), q)$ for every
 $(q_1, \ldots, q_p) \rightarrow^{\theta_1, \theta_2} q$ in δ_D ;

•
$$\vee_{q\in F_D} T(x,y,q) \rightarrow R(x,y);$$

plus reflexive-transitive closure axioms for R:

• R(x,x);

•
$$R(x,y) \wedge R(y,z) \rightarrow R(x,z)$$
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Proposition

$$\begin{array}{l} \textit{If } \tau \in \textit{L}(\textit{A}_{\textit{I}}) \textit{ then } \Phi \vdash \textit{Init}^{(1)}(t_{\tau}) \\ \textit{If } \tau \in \textit{L}(\textit{A}_{\textit{U}}) \textit{ then } \Phi \vdash \textit{Unsafe}^{(1)}(t_{\tau}) \\ \textit{If } \tau \in \textit{L}(\textit{A}_{\textit{I}}) \star \textit{R}^{*}_{\textit{D}} \textit{ then } \Phi \vdash \exists \textit{x} \textit{Init}^{(1)}(\textit{x}) \land \textit{R}(\textit{x}, t_{\tau}) \end{array} \right.$$

Corollary

(correctness of the verification method) If $\Phi \not\vdash \exists x \exists y (Init^{(1)}(x) \land R(x, y) \land Unsafe^{(1)}(y)$ then $(L(A_I) \star R_D^*) \cap L(A_U) = \emptyset$

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FCM verification method for RTMC

- In order to prove safety, that is (L(A_I) ★ R^{*}_D) ∩ L(A_U) = Ø it is sufficient to demonstrate
 Φ ∀ ∃x∃y(Init⁽¹⁾(x) ∧ R(x, y) ∧ Unsafe⁽¹⁾(y)
- In the FCM method we delegate this task to the generic finite model finding procedure, which searches for the finite countermodels for

$$\Phi \rightarrow \exists x \exists y (Init^{(1)}(x) \land R(x,y) \land Unsafe^{(1)}(y).$$

• If a countermodel is found the safety is established!

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Theorem (relative completeness of FCM)

Given an instance of the basic verification problem for RTMC, that is two tree automata A_I and A_U over an alphabet Σ and a tree transducer $D = (Q_D, F_D, \delta_D)$ over Σ^2 . If there exists a regular tree language \mathcal{R} such that $(L(A_I) \star R_D^*) \subseteq \mathcal{R}$ and $\mathcal{R} \cap L(A_U) = \emptyset$ then there is a finite countermodel for $\Phi \to \exists x \exists y (Init^{(1)}(x) \land R(x, y) \land Unsafe^{(1)}(y)$

It follows then that FCM is at least as powerful in establishing safety as RTMC, provided a complete finite model finding procedure is used.

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Parameterized Two-Way Token protocol:

- The system consists of finite-state processes connected to form a binary tree structure
- Each process stores a single bit which represents the fact that the process has a token
- During operation of the protocol the token can be passed up or down the tree.
- The correctness condition is that no two or more tokens ever appear (if started with one token)
- In parameterized verification we would like to establish correctness for all possible sizes of trees.

(see Demo)

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Lossy channel systems

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Lossy Channel Systems (LCS)

• Essentially finite state machines equipped with the channels

- during transitions the messages can be sent into the channels
- during transitions the messages can be read off the channels
- the messages can be lost
- but the order of the messages can not change
- Can be used to specify and verify the protocols on behavioral level
- Due to unbounded channels these are, generally infinite state systems
- Verification of safety is decidable Abdulla, Jonsson, 93

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Theorem

Parallel composition of

- complete theorem proving and
- complete finite model building

for first-order predicate logic provides with a decision procedure for safety properties of lossy channel systems.

ATVA 2010

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 Parameterized cache coherence protocols specified in terms of Extended FSM ATVA 2010

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- Parameterized cache coherence protocols specified in terms of Extended FSM ATVA 2010
- Safety verification for general term rewriting RTA 2012

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- Parameterized cache coherence protocols specified in terms of Extended FSM ATVA 2010
- Safety verification for general term rewriting RTA 2012

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• Consider the TRS $\mathcal{R} = \{f(x) \rightarrow f(s(s(x)))\}$ and assume that we want to prove that $f(a) \not\rightarrow^* f(s(a))$ (automatically)

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- Traditional approach (Genet & Rusu, 2010): use tree automata completion + finite state abstraction, expressed equationally (and added manually)

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- Traditional approach (Genet & Rusu, 2010): use tree automata completion + finite state abstraction, expressed equationally (and added manually)
- Alternative approach we advocate in this talk: translate the original question into a logical problem of disproving a first-order formlua (in classical predicate logic)

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• Let
$$\Phi = \{R(f(x), f(s(s(x))), R(x, y) \land R(y, z) \rightarrow R(x, z), R(x, y) \land R(f(x), f(y))\}$$

- Suppose to the contrary $f(a) \rightarrow^* f(s(a))$
- Then Φ ⊢ R(f(a), f(s(a))) holds in classial first-order logic (a derivation would follow rewriting)
- But one can easily find a finite countermodel for Φ → R(f(a), f(s(a)) automatically using a generic finite model finding procedure (e.g. implemented in Mace4)
- So, the original question is resolved positively

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Experimental results

| Protocol | Time |
|----------------|-------|
| ABP* | 0.93s |
| MSI | 0.01s |
| MESI | 0.03s |
| MOESI | 0.05s |
| Firefly | 0.03s |
| Synapse N $+1$ | 0.01s |
| Illinois | 0.03s |
| Berkeley | 0.03s |
| Dragon | 0.05s |
| Futurebus+ | 1.14s |
| Bakery | 0.01s |
| MutEx | 0.01s |

* ABP = Alternating Bit Protocol

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Monotonic abstraction and RMC problems.

| Protocol | Time |
|-------------------------------|-------|
| Token passing (non-optimized) | 0.12s |
| Token passing (optimized) | 0.01s |
| Mutual exclusion I | 0.03s |
| Mutual exclusion II | 341s |
| Bakery | 0.03s |
| Paterson ⁻ | 0.77s |

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FCM vs RTMC and Monotonic Abstraction

| Protocol | Time | Time reported for RTMC* |
|---------------|-------|-------------------------|
| Token | 0.02s | 0.06s |
| Two-way Token | 0.03s | 0.09s |

 * the system configuration was Intel Centrino 1.6GHZ with 768MB of RAM

| Protocol | Time | Time reported* |
|-----------------|-------|----------------|
| Token | 0.02s | 1s |
| Two-way Token | 0.03s | 1s |
| Percolate | 0.02s | 1s |
| Leader Election | 0.03s | 1s |
| Tree-arbiter | 0.02s | 37s |
| IEEE 1394 | 0.04s | 1h15m25s |

* the system configuration was *dual Opteron 2.8 GHZ with 8 GB* of *RAM*

• We presented FCM method for verification of parameterized systems

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- FCM is efficient in practice

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- Lossy Channel Systems ATVA 2010
- Cache Coherence Protocols ATVA 2010
- Linear Systems of Automata and Monotone Abstraction CoRR abs/1011.0447: (2010)
- Regular Model Checking CoRR abs/1011.0447: (2010)
- Regular Tree Model Checking CoRR abs/1107.5142:(2011), TTATT 2012 Workshop, Nagoya, RTA'12
- Safety for general TRS and Tree Automata Completion RTA 2012

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Thank you!